EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	7590	(SIMULAT$4 WITH CIRCUIT) AND DESIGN$4 AND PLAN$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:25
L10	791	L9 and (behav$5 with model)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:25
L11	251	L10 and (design with specification)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:25
L12	222	L11 and verif$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:25

EAST Search History

L13	220	L12 and input and output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:25
L14	12593	((703/13-22) or (716/1-21)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/09 12:25
L15	12593	((703/13-22) or (716/1-21)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/09 12:26
L16	294	L15 and (design$4 with circuit$2) and board and input and output and verif$7 and behav$5 and (design$4 with specification)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:26

EAST Search History

L17	294	L15 and (design$4 with circuit$2) and board and input and output and verif$7 and behav$5 and (design$4 with specification)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:26
L18	69	L17 and (floor with plan$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:26
L19	27	L18 and (customer and condition$2 and environment)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:26
L20	25	L19 and (frequency with operation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:31

EAST Search History

L21	25	20 and (simulat$4 with result$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:32
L24	22	20 and (receiv$4 and simulat$4 with result$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/09 12:33

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